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Date: September 6, 2005To: Examiner Lau
U.S. Patent OfficeFax No: 571-273-8300

Ph. No:

From: Andrew J. Telesz, Jr.Subject: Serial No.: 10/668,252
Our Ref. No.: OKI.571No. of Pages (including cover): 7.....
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CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8)Applicant(s): **Hitoshi Endo et al.**

Docket No.

OK1.571

Serial No.

10/668,252

Filing Date

September 24, 2003

Examiner

T. Lau

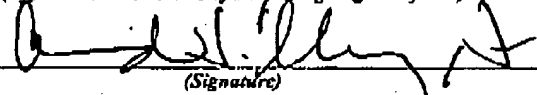
Group Art Unit

2863Invention: **TEST METHOD OF MEMORY IC FUNCTION ON DEVICE BOARD WITH DYNAMIC COMPETING CYCLE**

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**TRANSMITTAL LETTER
(General - Patent Pending)**Docket No.
OKI.571In Re Application Of: **Hitoshi Endo et al.**

Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/668,252	September 24, 2003	T. Lau	20987	2863	4296

Title: **TEST METHOD OF MEMORY IC FUNCTION ON DEVICE BOARD WITH DYNAMIC COMPETING CYCLE****COMMISSIONER FOR PATENTS:**

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Amendment Under 37 C.F.R. 1.312

in the above identified application.

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Serial No. 10/668,252
OKI.571

Amendment Under 37 C.F.R. 1.312 dated September 6, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION of

Hitoshi Endo et al.

Group Art Unit: 2863

Serial No.: 10/668,252

Examiner: T. Lau

Filed: September 24, 2003

Confirm. No.: 4296

For: TEST METHOD OF MEMORY IC FUNCTION ON DEVICE BOARD WITH
DYNAMIC COMPETING CYCLE

AMENDMENT UNDER 37 C.F.R. 1.312

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Sir:

Responsive to the Notice of Allowance dated August 17, 2005, the following amendments and remarks are respectfully submitted under 37 C.F.R. 1.312, in connection with the above-identified application.

Amendments to the Specification begin on page 2 of this paper.

Remarks/Arguments begin on page 3 of this paper.

Page 1 of 4